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LSI Logic Corporation
Legal Department - IP
1621 Barber Lane, MS D-106
Milpitas, CA 95035Date June 30, 2005Number of pages including cover sheet 2

To: US PTO

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TC 2131

Fax No. 703-872-9306

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

Telephone No. (408) 433-7191

Fax No. (408) 433-7460

REMARKS:

☐ Urgent☐ For your review☒ Reply ASAP☐ Please comment

Application Number: 10/667,021
 Filing date: September 18, 2003
 First named inventor: Robert Moss et al.
 Attorney docket number: 03-0172

Transmitted herewith for filing via facsimile:

- Status Request Letter

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Robert Moss et. al.

Serial No. : 10/667,021

Filed : September 18, 2003

For : **Methods and Structure For Scan
Testing of Secure Systems**

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**Group Art Unit :** 2131

**Examiner :**

**Atty Docket :** / 03-0172

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## STATUS REQUEST

**Commissioner for Patents**  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035  
[+1] 408-433-7191

Date: 6/30/05

Respectfully submitted,

Lenny Smith

Henry Groth, Ph: [+1] 408-433-7191

Reg. No. 39,696

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